AMENDMENTS TO CLAIMS:

The listing of claims will replace all prior versions, and listings of claims in the application:

LISTING OF CLAIMS:

1. (Currently amended) A method for displaying an image in an electronic medium, comprising:

utilizing a display controller to receive an image from a camera for display by:

storing a first set of dimension values in one or more registers in the display controller, the first set of dimension values specifying a display region in which a resized camera image is displayed;

storing a second set of dimension values in one or more registers in the display controller, the second set of dimension values specifying a picture-in-picture (PIP) window;

changing ~~a~~the first set of dimension values ~~associated with an image being displayed~~, wherein the resized camera image being displayed remains undisturbed when changing the first set of dimension values;

changing ~~a~~the second set of dimension values ~~associated with a display region in which the image is being displayed~~, wherein the ~~display region~~ picture-in-picture (PIP) window remains undisturbed when changing the second set of dimension values;

providing a completion signal indicating completion of changing the first set of dimension values and the second set of dimension values;

receiving a trigger signal indicating a beginning of a new image to be displayed; and

implementing the changed first set of dimension values and the changed second set of dimension values upon receiving the trigger signal while the completion signal is being provided.

2. (Original) A method for displaying an image in an electronic medium as recited in claim 1, wherein changing the first set of dimension values and the second set of dimension values spans a number of trigger signal receptions while the completion signal has not yet been provided.

3. (Original) A method for displaying an image in an electronic medium as recited in claim 1, further comprising:

operating image synchronization logic to recognize receipt of the trigger signal while the completion signal is being provided; and

operating the image synchronization logic to cause an essentially simultaneous implementation of the changed first set of dimension values and the changed second set of dimension values.

4. (Cancelled)

5. (Currently amended) A method for displaying an image in an electronic medium as recited in claim 41, wherein providing the completion signal is performed by setting an enable bit within a last register changed, wherein the last register changed represents a final register required to have its dimension value changed.

6. (Currently amended) A method for displaying an image in an electronic medium as recited in claim 41, wherein changing the first set of dimension values stored in the ~~four~~one or more registers ~~associated with camera image resizer logic~~specifying a display region in which a resized camera image is displayed is performed by duplicating changes made to the second set of dimension values stored in the ~~four~~one or more registers ~~associated with~~specifying the PIP window.

7. (Currently amended) A method for displaying an image in an electronic medium as recited in claim 1, wherein ~~the image is a camera image and~~ the trigger signal is a VSYNC signal indicating a new frame of the camera image.

8-14. (Cancelled)

15. (Original) A display controller for use in an electronic device having an image display capability, comprising:

a first set of dimension registers configured to store dimension values for resizing an image to be displayed, each of the first set of dimension registers including an enable bit;

a second set of dimension registers configured to store dimension values for defining a display region in which the image to be displayed, each of the second set of dimension registers including an enable bit; and

image synchronization circuitry configured to recognize an asserted enable bit setting in any dimension register of the first set of dimension registers and the second set of dimension registers, the image synchronization circuitry being further configured to implement dimension values stored in each of the first set of dimension registers and the second set of dimension registers upon both recognizing the asserted enable bit setting and receiving a trigger signal indicating a beginning of a new image to be displayed.

16. (Original) A display controller for use in an electronic device having an image display capability as recited in claim 15, wherein the image synchronization circuitry is configured to recognize a vertical synchronization signal as the trigger signal, the vertical synchronization signal to be provided in conjunction with image data to be received by the display controller.

17. (Original) A display controller for use in an electronic device having an image display capability as recited in claim 15, wherein the image synchronization circuitry is configured to implement the dimension values stored in each of the first set of dimension registers and the second set of dimension registers in a simultaneous manner.

18. (Original) A display controller for use in an electronic device having an image display capability as recited in claim 15, further comprising:

camera interface circuitry configured to receive data defining the image to be displayed; and

resizer circuitry configured to adjust a size of the image to be displayed in accordance with dimension values stored in the first set of dimension registers, the resizer circuitry further configured to be controlled by the image synchronization circuitry.

19. (Original) A display controller for use in an electronic device having an image display capability as recited in claim 15, wherein the display controller is incorporated into a portable electronic computing device.

20. (Original) A display controller for use in an electronic device having an image display capability as recited in claim 19, wherein the portable electronic computing device is selected from the group consisting of a cellular phone, a personal digital assistant, a web tablet, and a pocket personal computer.